

Lesson Planning for the semester started w.e.f. 5 January 2018

Name of Institute :- Sat Kabir Institute of Technology and Management, Ladrawan (Jhajjar)

Name of teacher with designation :- SUMIRAN A.P.

Department :- ECE

Month	Class	Topic/Chapter Covered	Academic Activity	Test/Assignment
JAN	4 <sup>th</sup>	<u>EMT</u> :- Co-ordinate system & transformation (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Divergence theorem & Laplacian of a scalar (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>DSD</u> :- Introduction to CAD tools (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Introduction to behavioral dataflow (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>MCES</u> :- Introduction to microcontroller (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Microcontroller memory types and features (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
FEB	4 <sup>th</sup>	<u>EMT</u> :- Electrostatics & properties of material (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Electrostatic boundary value problems (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>DSD</u> :- VHDL statements (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Application of functions & procedures (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>MCES</u> :- Microcontroller Architecture (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Addressing modes & Instruction set (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
MAR	4 <sup>th</sup>	<u>EMT</u> :- Magnetostatics & magnetic flux density (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Magnetic forces, Inductor & inductances (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>DSD</u> :- Combinational & Sequential circuit design (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		VHDL Models & Shift Registers (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>MCES</u> :- Introduction to 8051 microcontroller (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Assembly language programming (2 weeks)	Section Revision	Test in 4 <sup>th</sup> week

*Sumiran*  
Signature of the teacher concerned with date

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APR	4 <sup>th</sup>	<u>EMT</u> :- Waves and applications (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Electromagnetic wave propagation & TX lines (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>DSD</u> :- Design of micro computers (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		VHDL programmable devices (2 weeks)	Section revision	Test in 4 <sup>th</sup> week
	6 <sup>th</sup>	<u>MCS</u> :- Introduction to embedded systems (2 weeks)	PPT	Assignment in 3 <sup>rd</sup> week
		Interfacing of devices (2 weeks)	Section revision	Test in 4 <sup>th</sup> week

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JAN	6 <sup>th</sup>	DSD Lab :- 1 Design all gates using VHDL.		
		2 VHDL programs for half adder & full adder.	Practical to be performed	File to be checked
		3 VHDL programs for multiplexer & de-multiplexer.		
FEB	6 <sup>th</sup>	MCES Lab :- 1 ALP to generate 10KHz square wave		
		2 Implementation & Interfacing of display devices	Practical to be performed	File to be checked
		3 Implementation & interfacing of motors		
FEB	6 <sup>th</sup>	DSD Lab :- 1 VHDL programs for decoder & encoder		
		2 VHDL program for comparator	Practical to be performed	File to be checked.
		3 VHDL program for code converter.		
MAR	6 <sup>th</sup>	MCES Lab :- 1 ALP for temperature & pressure measurement.		
		2 Interfacing graphical LCD with 89C51.	Practical to be performed.	File to be checked
		3 Study & programming of serial & parallel port.		
MAR	6 <sup>th</sup>	DSD Lab :- 1 VHDL program for a FLIP-FLOP		
		2 VHDL program for a counter.	Practical to be performed	File to be checked
		3 VHDL program for register.		
APR	6 <sup>th</sup>	MCES Lab :- 1 Interface regulator using PIC microcontroller		
		2 Study of interfacing of graphical LCD.	Practical to be performed	File to be checked
		3 Interfacing of IR and RF communication		
APR	6 <sup>th</sup>	DSD Lab :- 1 VHDL program for shift register	Practical problems	File to be checked
	6 <sup>th</sup>	MCES Lab :- 1 Study of SD/MMC card interface using 18F4550.	Practical problems	File to be checked

  
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