

Lesson Planning for the semester started w.e.f.....5 January 2018.....

Name of Institute :- Sat Kabir Institute of Technology and Management, Ladrawan (Jhajjar)

Name of teacher with designation :- SUMAN KUMARI A.P.

Department :- ECE

Month	Class	Topic/Chapter Covered	Academic Activity	Test/Assignment
JAN	EE-6 th	<u>MCES</u> :- Introduction to microcontroller (2 weeks)	PPT	Assignment in 3 rd week
		Microcontroller memory types and features (2 weeks)	Section revision	Test in 4 th week
	CSE-6 th	<u>DSD</u> :- Introduction to CAD tools (2 weeks)	PPT	Assignment in 3 rd week
		Introduction to behavioral datapflow (2 weeks)	Section revision	Test in 4 th week
EE-6 th	<u>DSD</u> :- Introduction to CAD tools (2 weeks)		PPT	Assignment in 3 rd week
		Introduction to behavioral datapflow (2 weeks)	Section revision	Test in 4 th week
FEB	EE-6 th	<u>MCES</u> :- Microcontroller architecture (2 weeks)	PPT	Assignment in 3 rd week
		Addressing modes & instruction set (2 weeks)	Section revision	Test in 4 th week
	CSE-6 th	<u>DSD</u> :- VHDL statements (2 weeks)	PPT	Assignment in 3 rd week
		Application of functions & procedures (2 weeks)	Section revision	Test in 4 th week
EE-6 th	<u>DSD</u> :- VHDL statements (2 weeks)		PPT	Assignment in 3 rd week
		Application of functions & procedures (2 weeks)	Section revision	Test in 4 th week
MAR	EE-6 th	<u>MCES</u> :- Introduction to 8051 microcontroller (2 weeks)	PPT	Assignment in 3 rd week
		Assembly language programming (2 weeks)	Section Revision	Test in 4 th week
	CSE-6 th	<u>DSD</u> :- Combinational & Sequential circuit Design (2 weeks)	PPT	Assignment in 3 rd week
		VHDL Models & Shift Registers (2 weeks)	Section Revision	Test in 4 th week
EE-6 th	<u>DSD</u> :- Combinational & Sequential circuit Design (2 weeks)		PPT	Assignment in 3 rd week
		VHDL Models & Shift Registers (2 weeks)	Section Revision	Test in 4 th week

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JAN	EE-6 th	MCES Lab 1-1 ALP to generate 10KHZ Square wave	Practicals to be performed	File to be checked
		2 Implementation & interfacing of display devices 3 Implementation & interfacing of motors		
FEB	CSE-6 th	DSD Lab 1:- 1 Design all gates using VHDL.	Practicals to be performed	File to be checked
	EE-6 th	MCES Lab :- 1 ALP for temperature & pressure management.		
		2 VHDL programs for half adder & full adder 3 VHDL programs for multiplexer & de-multiplexer.		
MAR	EE-6 th	2 Interfacing graphical LCD with 89C51.	Practicals to be performed	File to be checked
		3 Study & programming of serial & parallel port.		
	CSE-6 th	DSD Lab :- 1 VHDL programs for decoder & encoder 2 VHDL program for comparators. 3 VHDL program for code converter.	Practicals to be performed	File to be checked
APR	EE-6 th	MCES Lab :- 1 Interface regulator using PIC microcontroller	Practicals to be performed	File to be checked
		2 Study of interfacing of graphical LCD. 3 Interfacing of IR and RF communication.		
	CSE-6 th	DSD Lab :- 1 VHDL program for a FUP-Flop. 2 VHDL program for a counter 3 VHDL program for a register.	Practicals to be performed	File to be checked
APR	EE-6 th	MCES Lab 1-1 Study of SD/MMC card interface using BF4550	Practicals to be performed.	File to be checked.
		2 Practical problems and viva.		
	CSE-6 th	DSD Lab 1-1 VHDL program for shift register. 2 Practical problems and viva.	Practicals to be performed.	File to be checked.

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Month	Class	Topic/Chapter Covered	Academic Activity	Test/Assignment
APR	EE-6 th	<u>MCS</u> :- Introduction to embedded systems (2 weeks) Interfacing of devices (2 weeks)	PPT Section Revision	Assignment in 3 rd week Test in 4 th week
	OBE-6 th	<u>DSD</u> :- Design of micro computers (2 weeks) VHDL programmable devices (2 weeks)	PPT Section Revision	Assignment in 3 rd week Test in 4 th week
	EE-6 th	<u>DSD</u> :- Design of micro computers (2 weeks) VHDL programmable devices (2 weeks)	PPT Section Revision	Assignment in 3 rd week Test in 4 th week

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